

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: : Chang et al.
Serial No.: : 10/823,513
Filed: : April 12, 2004
For: : Autorun for Integrated Circuit Memory Component
Examiner : Rampuria, Satish
TC/A.U. : 2191
Date: : April 19, 2010

The Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT/RESPONSE

In response to the Final Office Action dated December 17, 2009, please accept a Request for Continued Examination, enter the following amendments, and consider the following remarks.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 15 of this paper.